## DATA SHEET



## BY448 <br> Damper diode

## FEATURES

- Glass passivated
- High maximum operating temperature
- Low leakage current
- Excellent stability
- Available in ammo-pack.


## APPLICATIONS

- Damper diode in high frequency horizontal deflection circuits up to 16 kHz.


## DESCRIPTION

Rugged glass package, using a high temperature alloyed construction.

This package is hermetically sealed and fatigue free as coefficients of expansion of all used parts are matched.


Fig. 1 Simplified outline (SOD57) and symbol.

## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
| :--- | :--- | :--- | :---: | :---: | :---: |
| $\mathrm{V}_{\text {RSM }}$ | non-repetitive peak reverse voltage |  | - | 1650 | V |
| $\mathrm{~V}_{\text {RRM }}$ | repetitive peak reverse voltage |  | - | 1650 | V |
| $\mathrm{~V}_{R}$ | continuous reverse voltage |  | - | 1500 | V |
| $\mathrm{I}_{\text {FWM }}$ | working peak forward current | $\mathrm{T}_{\text {amb }}=50{ }^{\circ} \mathrm{C} ;$ PCB mounting <br> $($ see Fig 4); see Fig.2 | - | 4 | A |
| $\mathrm{I}_{\text {FRM }}$ | repetitive peak forward current |  | - | 8 | A |
| $\mathrm{I}_{\text {FSM }}$ | non-repetitive peak forward current | $\mathrm{t}=10$ ms half sinewave; <br> $\mathrm{T}_{\mathrm{j}}=\mathrm{T}_{\mathrm{j} \text { max }}$ prior to surge; <br> $\mathrm{V}_{R}=\mathrm{V}_{\text {RRMmax }}$ | - | 30 | A |
| $\mathrm{~T}_{\text {stg }}$ | storage temperature |  | -65 | +175 | ${ }^{\circ} \mathrm{C}$ |
| $\mathrm{T}_{\mathrm{j}}$ | junction temperature |  | -65 | +150 | ${ }^{\circ} \mathrm{C}$ |

## ELECTRICAL CHARACTERISTICS

$\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$; unless otherwise specified.

| SYMBOL | PARAMETER | CONDITIONS | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{F}}$ | forward voltage | $\mathrm{I}_{\mathrm{F}}=3 \mathrm{~A} ; \mathrm{T}_{\mathrm{j}}=\mathrm{T}_{\mathrm{j} \text { max }}$; see Fig. 3 | 1.45 | V |
|  |  | $\mathrm{I}_{\mathrm{F}}=3 \mathrm{~A}$; see Fig. 3 | 1.60 | V |
| $\mathrm{I}_{\mathrm{R}}$ | reverse current | $\mathrm{V}_{\mathrm{R}}=\mathrm{V}_{\text {Rmax }} ; \mathrm{T}_{\mathrm{j}}=150^{\circ} \mathrm{C}$ | 150 | $\mu \mathrm{A}$ |
| $\mathrm{t}_{\mathrm{rr}}$ | reverse recovery time | when switched from $I_{F}=0.5 \mathrm{~A}$ to $\mathrm{I}_{\mathrm{R}}=1 \mathrm{~A}$; measured at $I_{R}=0.25 \mathrm{~A}$; see Fig. 6 | 1 | $\mu \mathrm{s}$ |
| $\mathrm{t}_{\mathrm{fr}}$ | forward recovery time | when switched to $I_{F}=4 \mathrm{~A}$ in 50 ns ; $\mathrm{T}_{\mathrm{j}}=\mathrm{T}_{\mathrm{j} \text { max }}$; see Fig. 7 | 1 | $\mu \mathrm{S}$ |

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## THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | CONDITIONS | VALUE | UNIT |
| :--- | :--- | :--- | ---: | :---: |
| $\mathrm{R}_{\text {th } j \text {-tp }}$ | thermal resistance from junction to tie-point | lead length $=10 \mathrm{~mm}$ | 46 | $\mathrm{~K} / \mathrm{W}$ |
| $\mathrm{R}_{\text {th } j \text { j-a }}$ | thermal resistance from junction to ambient | note 1 | 100 | $\mathrm{~K} / \mathrm{W}$ |
|  |  | mounted as shown in Fig. 5 | 55 | $\mathrm{~K} / \mathrm{W}$ |

## Note

1. Device mounted on an epoxy-glass printed-circuit board, 1.5 mm thick; thickness of Cu-layer $\geq 40 \mu \mathrm{~m}$, see Fig. 4 . For more information please refer to the "General Part of associated Handbook".

## GRAPHICAL DATA



Solid line: basic high-voltage E/W modulator circuit; see Fig.8.
Dotted line: basic conventional horizontal deflection circuit; see Fig.9.
Curves include power dissipation due to switching losses.
Fig. 2 Maximum total power dissipation as a function of the working peak forward current.


Dimensions in mm.
Fig. 4 Device mounted on a printed-circuit board.


Dotted line: $T_{j}=150^{\circ} \mathrm{C}$.
Solid line: $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$.
Fig. 3 Forward current as a function of forward voltage; maximum values.




Input impedance oscilloscope: $1 \mathrm{M} \Omega, 22 \mathrm{pF} ; \mathrm{t}_{\mathrm{r}} \leq 7 \mathrm{~ns}$.
Source impedance: $50 \Omega$; $\mathrm{t}_{\mathrm{r}} \leq 15 \mathrm{~ns}$.
Fig. 6 Test circuit and reverse recovery time waveform and definition.


Fig. 7 Forward recovery time definition.

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## APPLICATION INFORMATION

For horizontal deflection circuits, two basic applications are shown in Figs 8 and 9.
The maximum allowable total power dissipation for the diode can be calculated from the thermal resistance $\mathrm{R}_{\text {th }} j$-a and the difference between $\mathrm{T}_{\mathrm{j} \text { max }}$ and $\mathrm{T}_{\text {amb max }}$ in the application. The maximum $\mathrm{I}_{\mathrm{FWM}}$ can then be taken from Fig. 2 .
The basic application waveforms in Fig. 10 relate to the circuit in Fig.8. In the circuit in Fig. 9 the forward conduction time of the diode is shorter, allowing a higher $\mathrm{I}_{\text {FWM }}$ (see Fig.2).


Fig. 8 Application in basic high-voltage E/W modulator circuit.



Fig. 10 Basic application waveforms.

## Damper diode

## PACKAGE OUTLINE

$\square$

## DEFINITIONS

| Data Sheet Status |  |
| :--- | :--- |
| Objective specification | This data sheet contains target or goal specifications for product development. |
| Preliminary specification | This data sheet contains preliminary data; supplementary data may be published later. |
| Product specification | This data sheet contains final product specifications. |
| Limiting values |  |
| Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or <br> more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation <br> of the device at these or at any other conditions above those given in the Characteristics sections of the specification <br> is not implied. Exposure to limiting values for extended periods may affect device reliability. |  |
| Application information | Where application information is given, it is advisory and does not form part of the specification. |

## LIFE SUPPORT APPLICATIONS

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